



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of: Craig L. Stevens et al.	
Serial No: 09/872,796 <b>COPY OF PAPERS ORIGINALLY FILED</b>	Examiner: not yet known
Filed: 06/01/2001	Art Unit: 2812
Title: Architecture For High Throughput Semiconductor Processing Applications	Atty. Docket No.: 10001.000600 (NVLS 379)  <i>HL10</i> <i>Informal Drawing</i> <i>Littleton</i> <i>677-er</i>

Commissioner for Patents  
Washington, D.C. 20231

**LETTER TO THE OFFICIAL DRAFTSPERSON  
(Request To Substitute Drawings)**

Sir:

Subject to the approval of the Primary Examiner in the above-entitled patent application, please substitute the enclosed 15 sheets of drawings, containing Figures 1A, 1B, 2, 3, 4, 5, 6, 7, 8, 9A, 9B, 9C, 9D, 10A, and 10B, for the 15 sheets of informal drawings containing Figures 1A, 1B, 2, 3, 4, 5, 6, 7, 8, 9A, 9B, 9C, 9D, 10A, and 10B originally filed with the application.

**REMARKS**

Applicants respectfully submit that the substitute drawings are consistent with the corresponding material in the specification and originally filed drawings; the substitute drawings do not add any new matter to the application.